What is claimed is:

1. A system, comprising:

a first voltage regulator having a supply input coupled to a first supply voltage, an enable input and a supply output, the first voltage regulator selectively providing at the supply output up to a first predetermined current level at a regulated voltage based upon the first supply voltage when enabled and providing substantially no current when disabled;

compare circuitry having an input coupled to a first supply voltage and an output coupled to the enable input of the first voltage regulator and having a value indicative of whether the first supply voltage is greater than a predetermined voltage level; and

circuitry having a supply input coupled to the supply output of the first voltage regulator.

2. The system of claim 1, further comprising a second voltage regulator having a supply input coupled to a second supply voltage when enabled and a supply output, the second voltage regulator selectively providing at the supply output

thereof up to a second predetermined current level at a regulated voltage based upon the second supply voltage, the supply output of the first voltage regulator being coupled to the supply output of the second voltage regulator.

3. The system of claim 2, wherein the supply input of the first voltage regulator is coupled to the supply input of the second voltage regulator.

4. The system of claim 2, wherein the supply input of the first voltage regulator and the supply input of the second voltage regulator are coupled to an external power supply.

5. The system of claim 4, wherein the supply input of the second voltage regulator is coupled to the external power supply and a battery.

6. The system of claim 2, wherein the regulated voltage provided by the second voltage regulator is less than

the regulated voltage provided by the first voltage regulator.

7. The system of claim 1, wherein the first voltage regulator comprises a first transistor having a first conduction terminal coupled to the supply input thereof, a second conduction terminal coupled to the supply output of the first voltage regulator and a control terminal, the transistor providing to the supply output of the first voltage regulator the first predetermined current level.

8. The system of claim 7, further comprising biasing circuitry coupled to a control terminal of the first transistor, wherein the first transistor operates in a saturation mode of operation when enabled.

9. The system of claim 7, wherein the first voltage regulator further comprises a second transistor having a first conduction terminal coupled to the supply input of the first voltage regulator, a control terminal coupled to the

output of the compare circuitry and a second conduction terminal coupled to the control terminal of the first transistor

10. The system of claim 1, further comprising a transistor having a first conduction terminal coupled to a battery, a second conduction terminal coupled to the supply input of the circuitry and a control terminal coupled to the output of the compare circuitry.

11. The system of claim 1, wherein the circuitry comprises a volatile memory.

12. A method for providing a supply voltage to a circuit, comprising:

receiving a first supply voltage;

comparing the first supply voltage to a predetermined voltage level; and

selectively enabling a regulator circuit based upon the comparison, the regulator circuit providing up to a first predetermined current level at a first regulated voltage to the circuit when enabled, the first regulated voltage being based upon the first supply voltage.

13. The method of claim 12, further comprising selectively coupling a battery to the circuit based upon the comparison.

14. The method of claim 13, further comprising regulating the voltage provided by the battery to generate a second regulated voltage, and supplying the second regulated voltage to the circuit.

15. The method of claim 14, wherein the second regulated voltage is less than the first regulated voltage.

16. A device, comprising:

a first voltage regulator having a supply input, an enable input and a supply output, the first voltage regulator receiving a supply voltage at the supply input and providing at the supply output a regulated voltage at up to a first predetermined current level when enabled and providing substantially no current when disabled.

17. The device of claim 16, wherein the first regulator comprises a first transistor having a first conduction terminal coupled to the supply input thereof, a second conduction terminal coupled to the supply output of the first voltage regulator and a control terminal, the transistor providing to the supply output of the first voltage regulator up to the first predetermined current level.

18. The device of claim 17, further comprising biasing circuitry coupled to the control terminal of the first

transistor for providing a predetermined biased voltage thereto, wherein the first transistor operates in a saturation mode of operation when activated.

19. The device of claim 17, wherein the first voltage regulator further comprises a second transistor having a first conduction terminal coupled to the supply input of the first voltage regulator, a control terminal coupled to the enable input and a second conduction terminal coupled to the control terminal of the first transistor.

20. The device of claim 16, further comprising a second voltage regulator having a supply input and a supply output, the second voltage regulator selectively providing at the supply output thereof up to a second predetermined current level at a regulated voltage, the supply output of the first voltage regulator being coupled to the supply output of the second voltage regulator.

21. The device of claim 20, wherein the supply input of the first voltage regulator is coupled to the supply input of the second voltage regulator.

22. The device of claim 20, wherein the supply input of the first voltage regulator and the supply input of the second voltage regulator are coupled to an external power supply.

23. The device of claim 20, wherein the supply input of the second voltage regulator is coupled to the external power supply and a battery.

24. The device of claim 20, wherein the regulated voltage provided by the second voltage regulator is less than the regulated voltage provided by the first voltage regulator.

25. The device of claim 16, further comprising a compare circuit having an input adapted to be coupled to a voltage supply, for generating a signal at an output of the

compare circuit having a value indicative of the voltage appearing at the input being greater than a predetermined reference voltage, the output of the compare circuit being coupled to the enable input of the first voltage regulator.

26. The device of claim 25, further comprising a transistor having a first conduction terminal coupled adapted to be coupled to a battery, a second conduction terminal coupled to the output of the first voltage regulator and a control terminal coupled to the output of the compare circuit.

27. The device of claim 16, further comprising a volatile memory having a supply input coupled to the output of the first voltage regulator.